

FIG. 1 is a block diagram of a system for debugging a target device. The system includes a Debug Controller (1), a Discrimination Device (3), and a target device (2). The Debug Controller (1) is connected to the Discrimination Device (3) and the target device (2). The target device (2) includes a CPU (2-1), a Debug I/F (2-2), a PORT (2-3), a Send Circuit (2-4), a Shift Register (2-6), a Collate Detector (2-7), a Timer (2-8), and a Peripheral (2-12). The system also includes an OSC (Oscillator) and a DRSTZ901 (Reset Zener Diode).

FIG. 1

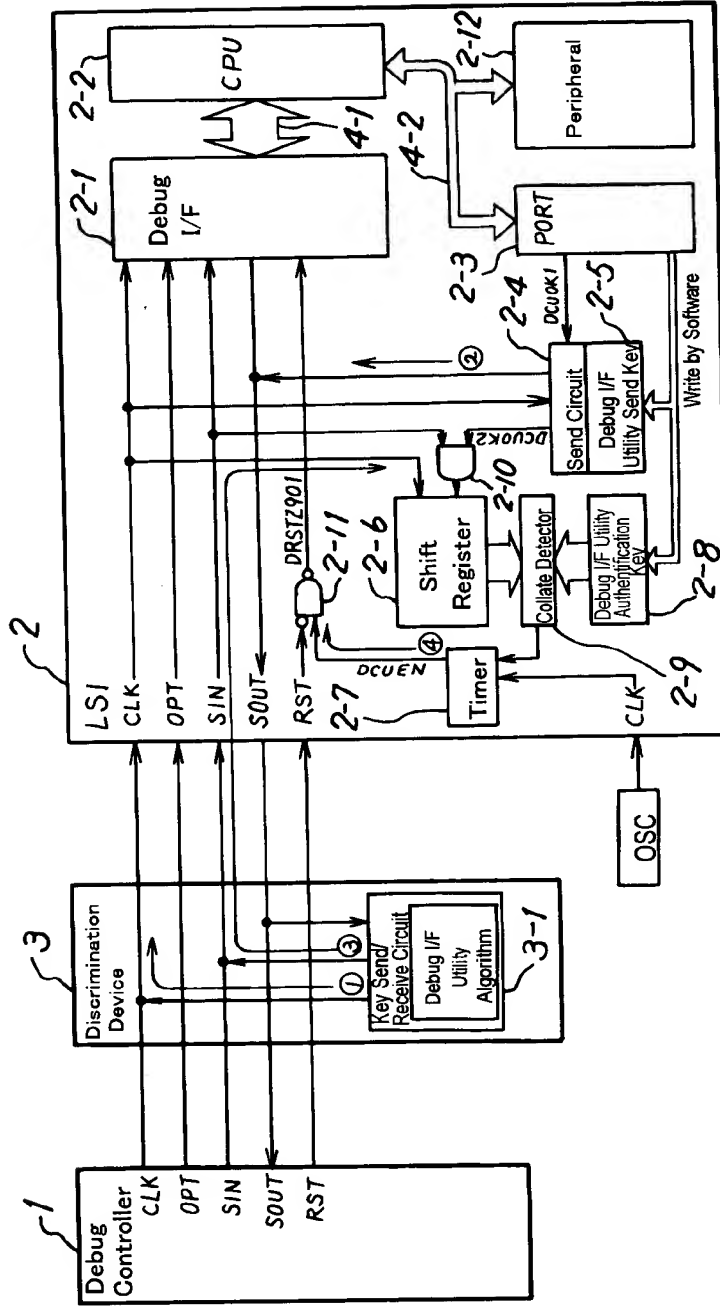


FIG. 2

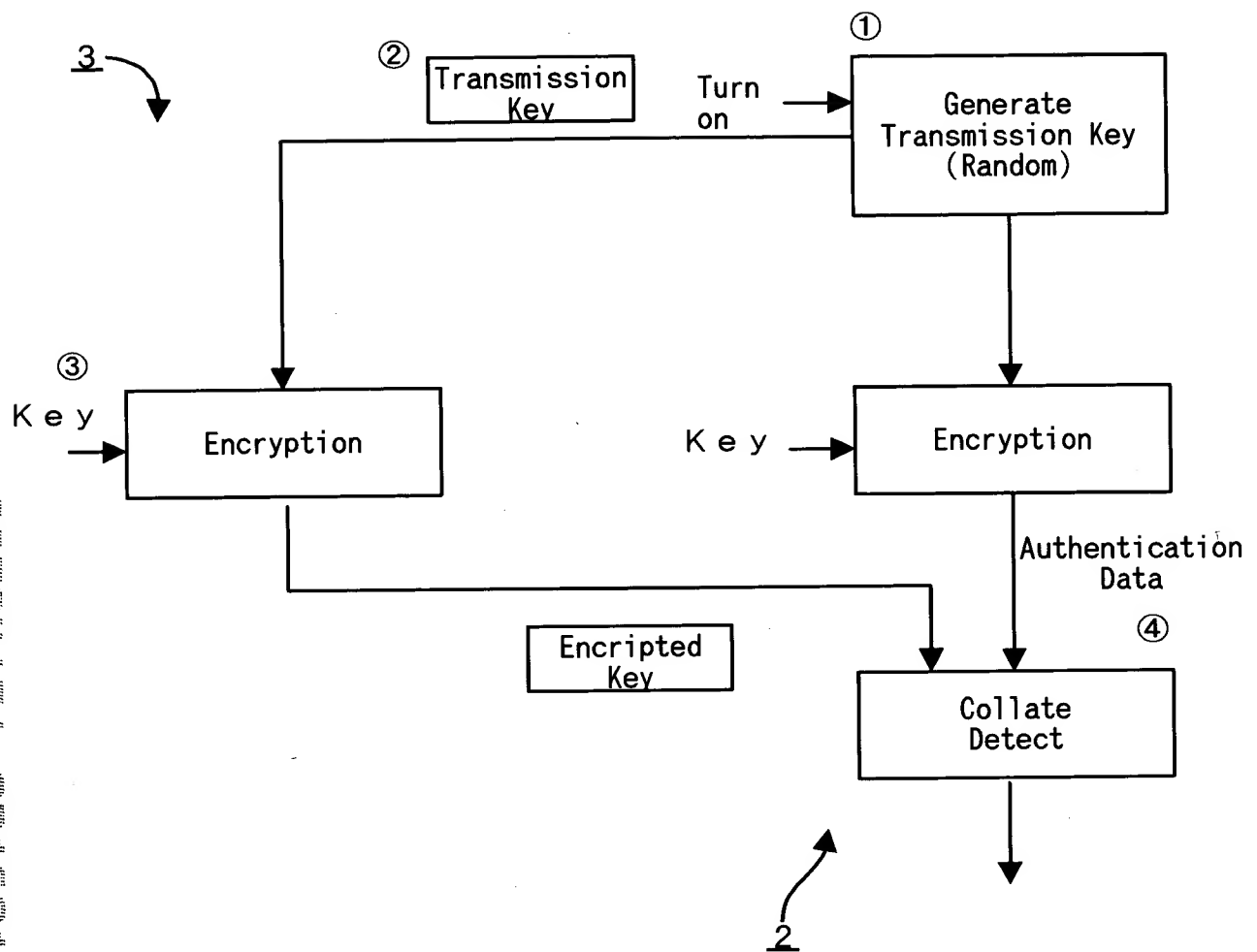


FIG. 3

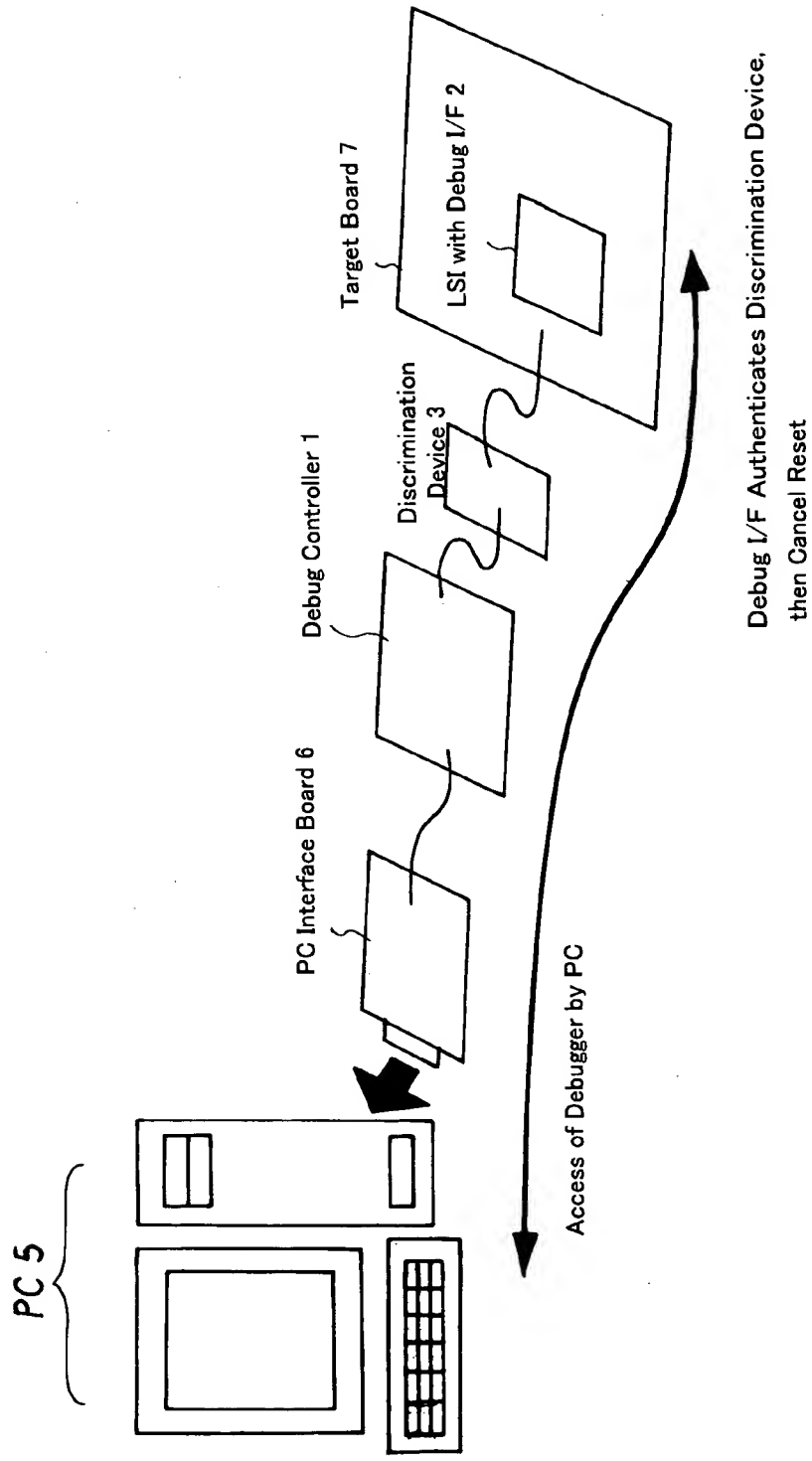


FIG. 4

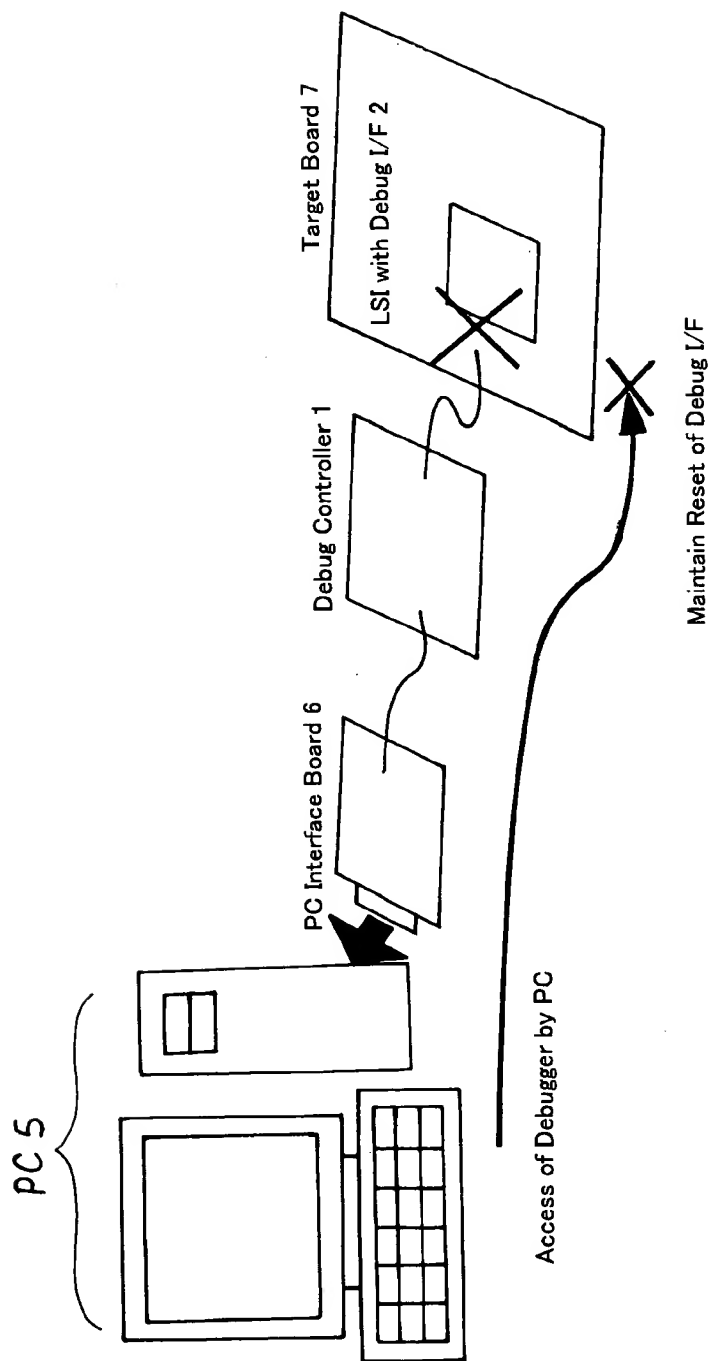


FIG. 5

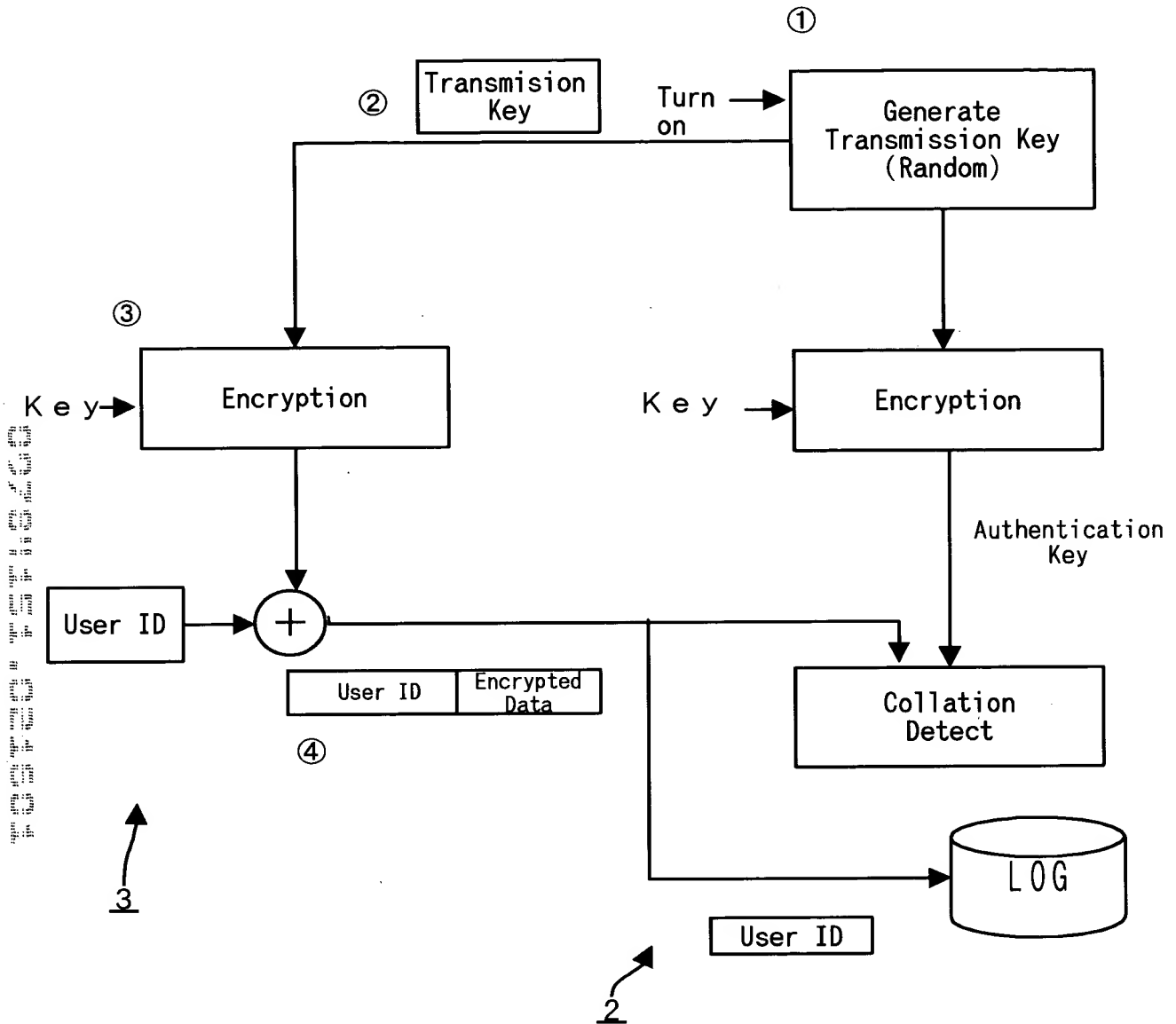
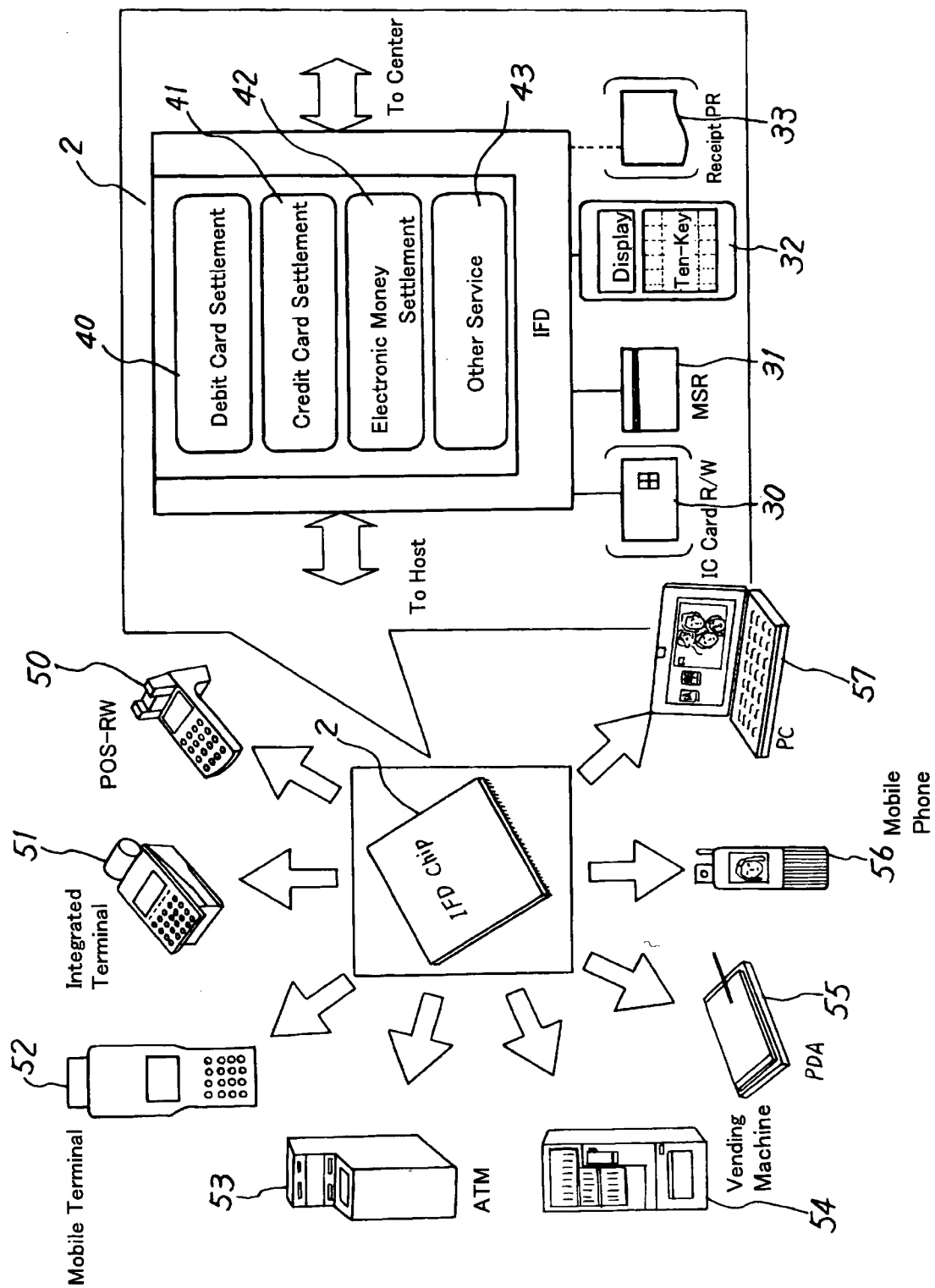
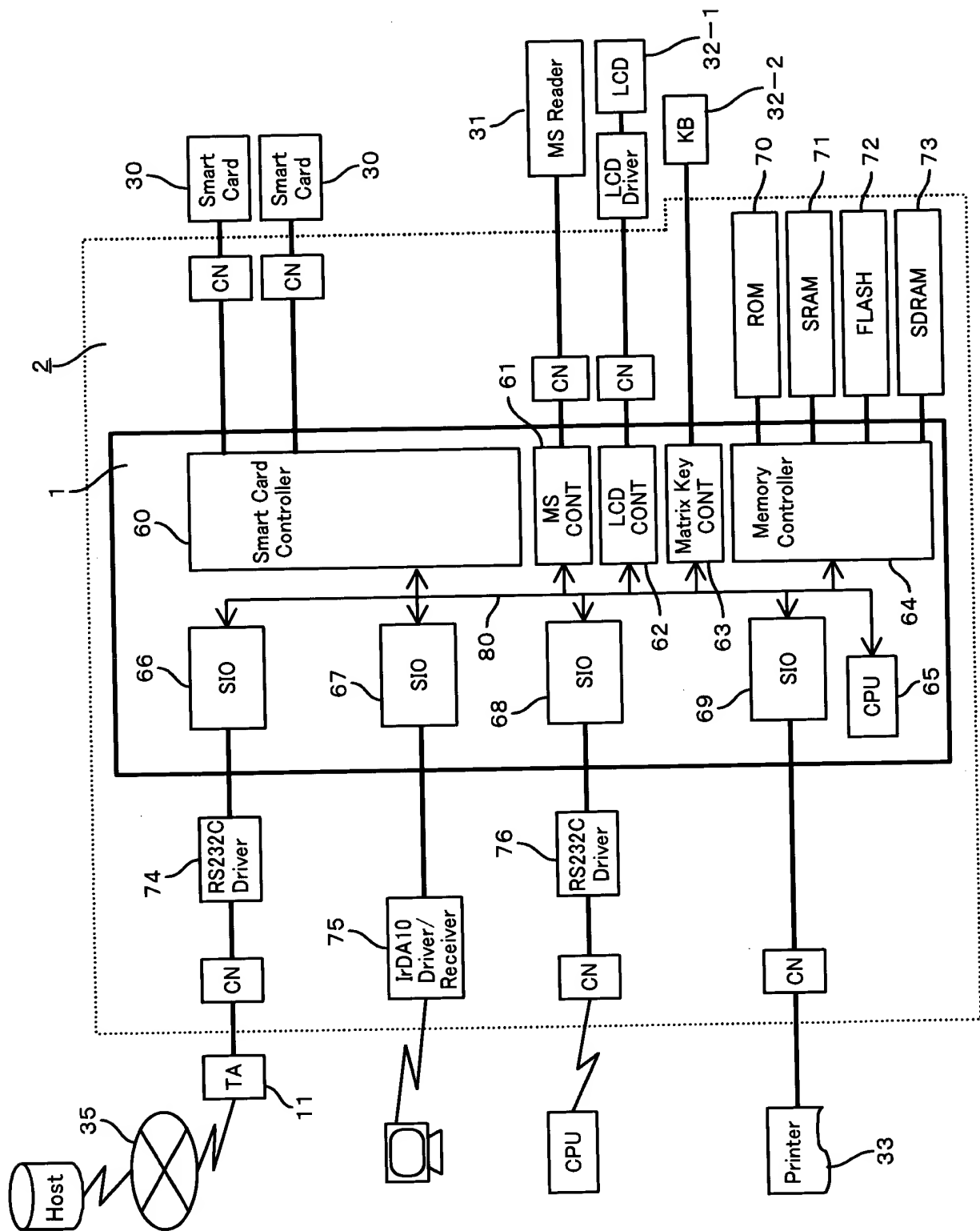


FIG. 6



1. *What is the purpose of the study?*
 2. *What are the research questions or hypotheses?*
 3. *What is the study design?*
 4. *What is the sample size and selection method?*
 5. *What are the variables being measured?*
 6. *What are the data collection methods?*
 7. *What are the results of the study?*
 8. *What are the conclusions and implications of the study?*
 9. *What are the limitations of the study?*
 10. *What are the strengths of the study?*
 11. *What are the future research directions?*
 12. *What are the ethical considerations?*
 13. *What are the funding sources?*
 14. *What are the conflicts of interest?*
 15. *What are the acknowledgments?*
 16. *What are the references?*
 17. *What are the appendices?*
 18. *What are the footnotes?*
 19. *What are the tables and figures?*
 20. *What are the conclusions?*



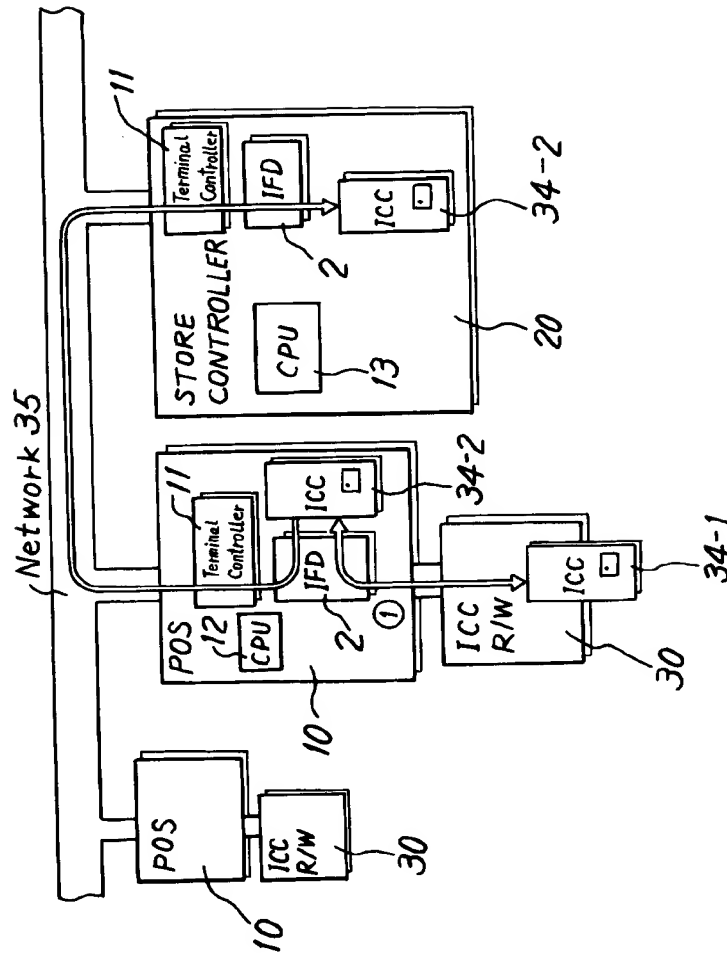
[illegible]

FIG. 9

Prior Art

